1/7 20 SHIFT CIRCUI CONTROL CIRCUIT ADC INTEGRATING V20 CIRCUIT SW3m SW31 SW41 INTEGRATING V20 C INTEGRATING CIRCUIT Level Level Level O Q Ø (**XM** (<u>*</u>

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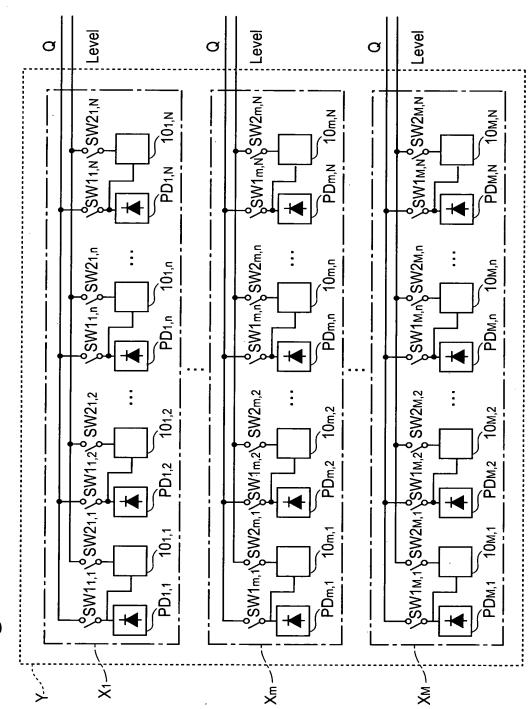
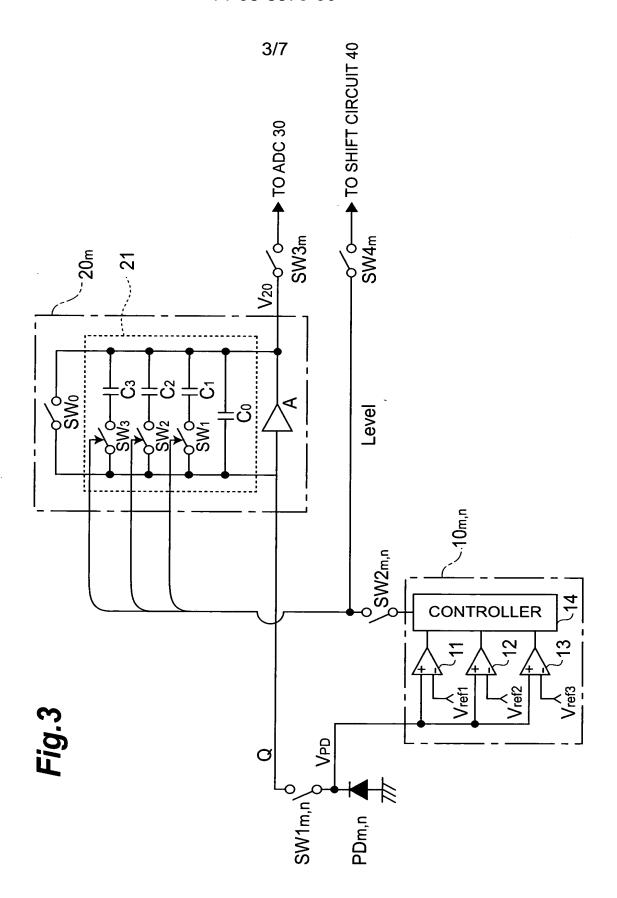


Fig.2





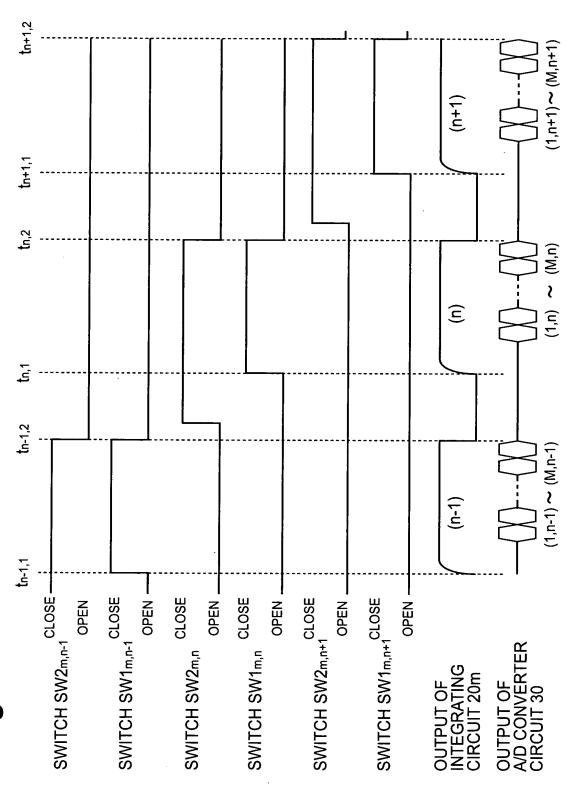
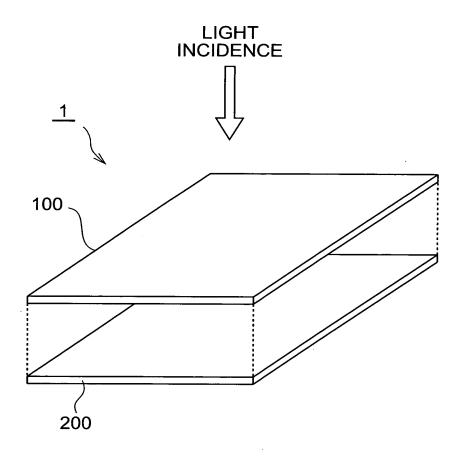


Fig.4

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Fig.5



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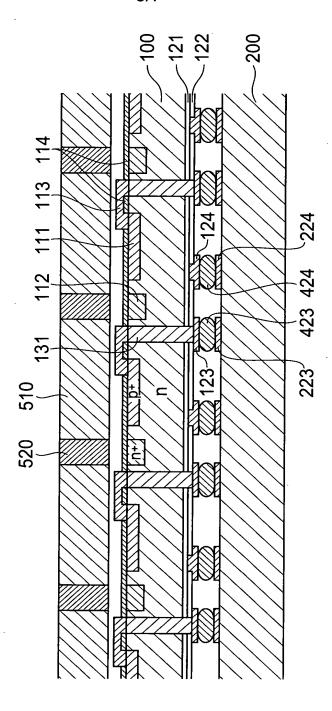


Fig.6

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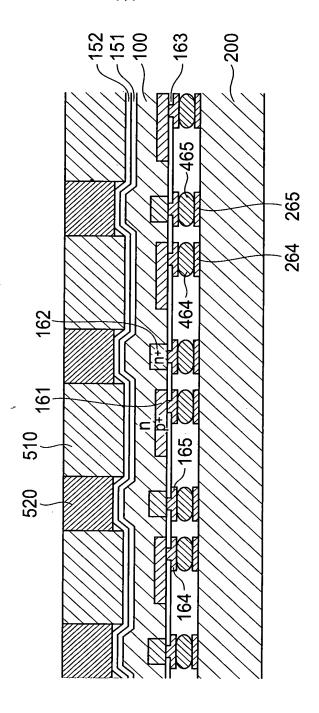


Fig.7